

Atty. Dkt. No. 02CR377/KE

Listing of claims:

1. (Original) A method of compensating for delays induced by anti-jamming processing, the method comprising:
determining a delay error associated with the anti-jamming processing; and
adjusting ionospheric corrections in response to the delay error.
2. (Original) The method of claim 1, wherein the delay is calculated in a distortion calculator associated with a digital anti-jamming processing circuit.
3. (Original) The method of claim 2, wherein a distortion calculator determines the delay error in response to weight values utilized by the digital anti-jamming processing circuit.
4. (Original) The method of claim 2, wherein the delay error is provided as serial data from the anti-jamming processing circuit.
5. (Original) The method of claim 4, wherein the serial data indicates a delay error parameter at a specific time period.
6. (Original) The method of claim 1, wherein the delay error parameter is utilized to adjust raw pseudo range values before ionospheric corrections are made.
7. (Original) The method of claim 3, wherein the distortion calculator is implemented in a digital signal processor.
8. (Original) The method of claim 1, wherein a derivative of the delay error is used to adjust the ionospheric corrections.
9. (Original) A positioning system including an anti-jamming processing circuit, the positioning system comprises;
a distortion calculator for calculating a delay error associated with the digital anti-jamming processing circuit; and

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an ionospheric correction circuit for providing ionospheric corrections for pseudo range values derived from data provided by the digital anti-jamming processing circuit, wherein the ionospheric corrections circuit calculates the ionospheric corrections in response to the delay error to reduce errors induced from the digital anti-jamming processing circuit.

10. (Original) The system of claim 9, where in the digital anti-jamming processing circuit is implemented in an application specific integrated circuit.

11. (Original) The system of claim 10, wherein the digital anti-jamming processing circuit includes a digital signal processor for implementing the distortion calculator and the application specific integrated circuit includes a weight application circuit.

12. (Original) The system of claim 9, wherein the ionospheric correction circuit receives raw pseudo ranges and adjusts the raw pseudo ranges in accordance with the delay error before creating corrected pseudo range values.

13. (Original) The system of claim 9, wherein the ionospheric correction circuit is included in a GPS receiver.

14. (Original) The system of claim 9, wherein the delay error is provided as serial data.

15. (Original) The system of claim 9, wherein the digital anti-jamming processing circuit is a space time adaptive processing circuit or space frequency adaptive processing circuit.

16. (Original) An apparatus for removing anti-jamming induced errors from ionospheric corrections, the apparatus comprising:
means for determining a delay error associated with anti-jamming processing; and
means for calculating ionospheric corrections, the means for calculating ionospheric corrections either receiving pseudo range values adjusted by the delay error or calculates the ionospheric corrections in accordance with the delay error.

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17. (Original) The apparatus of claim 16, wherein the means for determining a delay error is a digital signal processor.

18. (Original) The apparatus of claim 17, wherein when the pseudo range values are associated with a GPS.

19. (Original) The apparatus of claim 17, wherein the anti-jamming processing utilizes a beam forming algorithm.

20. (Original) The apparatus of claim 19, wherein the beam forming algorithm utilizes space time adaptive processing and/or space frequency adaptive processing.